

EAST - [default.wsp:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

Failed

L1: (1) (second near2 chip) same busy sam

L2: (1) memory and ((second near2 chip) s

L3: (155) memory and (second near2 chip)

L4: (1) memory and (second near2 chip) an

L5: (73) (memory same (second near2 chip

DBs

USPAT:US-PGPUB,EPO,JPO

Default operator: OR

Builds

Highlight all hit terms initially

BRS.htm

ISR.htm

Query

Text

HTML

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	1	(second near2 chip) same busy same ready same indicat\$3 same distinct	USPAT: US-PGPUB: 20 EPO: JPO
2	BRS	L2	1	memory and ((second near2 chip) same busy same ready same indicat\$3 same distinct)	USPAT: US-PGPUB: 20 EPO: JPO
3	BRS	L3	155	memory and (second near2 chip) and busy and ready and indicat\$3 and distinct	USPAT: US-PGPUB: 20 EPO: JPO
4	BRS	L4	1	memory and (second near2 chip) and (busy same ready same indicat\$3 same distinct)	USPAT: US-PGPUB: 20 EPO: JPO
5	BRS	L5	73	(memory same (second near2 chip)) and busy and ready and indicat\$3 and distinct	USPAT: US-PGPUB: 20 EPO: JPO

Ready

NUM

EAST - [default.wsp:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

L1: (1) (second near3 chip) same (first near

L2: (37) ((second near3 chip) same (first ne

Failed

Saved

Favorites

Tagged (3)

DBs

USPAT;US-PGPUB;EPO;JPO

Default operator: OR

Highlight all hit terms initially

USPAT;US-PGPUB;EPO;JPO

Default operator: OR

Highlight all hit terms initially

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	1	((second near3 chip) same (first near3 chip) same enable same respons\$3 same ready same busy	USPAT; US-PGPUB; 20 EPO; JPO
2	BRS	L2	37	((second near3 chip) same (first near3 chip)) and (enable same respons\$3) and (indicat\$3 same ready same busy)	USPAT; US-PGPUB; 20 EPO; JPO

DetailsHTML

Ready

NUM